

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hajime Kimura et al. Art Unit : 2629
 Serial No. : 10/756,756 Examiner : Jeffrey Piziali
 Filed : January 14, 2004 Conf. No. : 1526
 Title : A CURRENT SOURCE CIRCUIT, A SIGNAL LINE DRIVER CIRCUIT AND A
 DRIVING METHOD THEREOF AND A LIGHT EMITTING DEVICE

MAIL STOP AMENDMENT

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request. A copy of a communication from a foreign patent office in a counterpart application is also enclosed.

Submitted herewith is an English translation of the following foreign language references, or portions thereof:

Desig. ID	Source
AY	JAPAN 2001-056664 -- ABSTRACT
AZ	JAPAN 2002-221936 -- ABSTRACT
AAA	JAPAN 2002-251166 -- ABSTRACT

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. The fee in the amount of \$180 in payment of the late submission fee of §1.17(p) is being paid concurrently herewith on the Electronic Filing

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Attorney's Docket No.: 12732-207001 / US6910

System (EFS) by way of Deposit Account authorization. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 5/24/07


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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-207001	Application No. 10/756,756
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant Hajime Kimura et al.	
		Filing Date January 14, 2004	Group Art Unit 2629
(37 CFR §1.98(b))			

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	2002/0047581	04/25/2002	KOYAMA			10/24/2001
	AB	2002/0149608	10/17/2002	BU et al.			04/15/2002
	AC	2002/0180369	12/05/2002	KOYAMA			02/20/2002
	AD	2004/0085270	05/06/2004	KIMURA			10/30/2002
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	AF	2004/0232952	11/25/2004	KIMURA et al.			01/14/2004
	AG	2004/0239599	12/02/2004	KOYAMA			07/02/2004
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	AI	2006/0028413	02/09/2006	KIMURA			09/09/2005
	AJ	3,982,172	09/21/1976	VAN DE PLASSCHE			04/16/1975
	AK	4,967,140	10/30/1990	GROENEVELD et al.			07/14/1989
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	AO						
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	AQ						
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	AS						
	AT						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AU	0359315	03/21/1990	Europe			In English	
	AV	1202242	05/02/2002	Europe			In English	
	AW	1486942	12/15/2004	Europe			In English	
	AX	1577869	09/21/2005	Europe			In English	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
	AY	2001-056664	02/27/2001	Japan			Abstract	
	AZ	2002-221936	08/09/2002	Japan			Abstract	
	AAA	2002-251166	09/06/2002	Japan			Abstract	

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	ABB	GROENEVELD D. et al., "A Self-Calibration Technique for Monolithic High-Resolution D/A Converters", IEEE Journal of Solid-State Circuits, 1989, Vol. 24, No. 6, pp. 1517-1522
	ACC	TAN S. et al., "32.4: Designing of Circuit Building Blocks for OLED-on-Silicon Microdisplays", SID DIGEST 02: SID International Symposium Digest of Technical Papers, 2002, pp. 980-983
	ADD	Supplementary European Search Report (Application No. EP 03780930.8, PCT/JP0316354 dated February 20, 2007), 5 pages.
	AEE	

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